

PATENT
2332-0117P#6/ Suppl
Ambr
B
9/17/02
VJ

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: KIM, Jeong Soo, et al. Conf. No.: 4564
Appl. No.: 09/998,134 Group: 2823
Filed: December 3, 2001 Examiner: Thanh V. PHAM
For: METHOD OF FORMING A JUNCTION IN SEMICONDUCTOR
DEVICE USING HALO IMPLANT PROCESSING (To Be
Amended)

SUPPLEMENTAL AMENDMENT

Honorable Commissioner of Patents
Washington, D.C. 20231

August 22, 2002

Sir:

In response to the Examiner's Office Action dated May 8, 2002, the following supplemental amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE TITLE

Please replace the present title with the following:

--METHOD OF FORMING A JUNCTION IN SEMICONDUCTOR DEVICE
USING HALO IMPLANT PROCESSING--